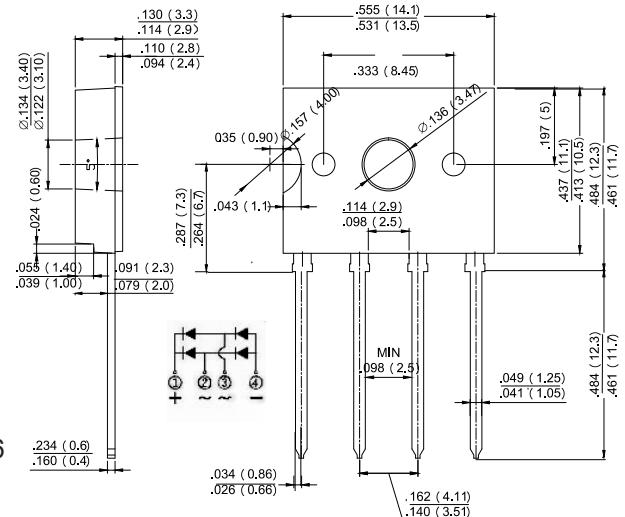




## SILICON BRIDGE RECTIFIERS

## Features

- ◆ Glass passivated die construction
- ◆ Low forward voltage drop
- ◆ High current capability
- ◆ High surge current capability
- ◆ Designed for surface mount application
- ◆ Plastic material-UL flammability 94V-O

D3K  
RoHS  
COMPLIANT

## Mechanical Data

**Case :** JEDEC D3K Molded plastic body**Terminals :** Solder plated, solderable per MIL-STD-750, Method 2026**Polarity :** Polarity symbol marking on body**Mounting Position :** Any

Dimensions in inches and (millimeters)

## Maximum Ratings And Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

Parameter	SYMBOLS	MDD UG2KB05	MDD UG2KB10	MDD UG2KB20	MDD UG2KB40	MDD UG2KB60	MDD UG2KB80	MDD UG2KB100	UNITS
Marking Code									
Maximum repetitive peak reverse voltage	V <sub>RPM</sub>	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	V
Maximum average forward output rectified current at TA=40°C	I <sub>(AV)</sub>								A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>								A
Maximum instantaneous forward voltage drop per bridge element at 2.0A	V <sub>F</sub>								V
Maximum DC reverse current TA=25°C at rated DC blocking voltage TA=100°C	I <sub>R</sub>								µA
Typical Junction Capacitance	C <sub>J</sub>								pF
Typical Thermal Resistance R <sub>θJA</sub> R <sub>θJL</sub>									°C/W
Operating junction temperature range	T <sub>J</sub>								°C
storage temperature range	T <sub>STG</sub>								°C

Note:1. Mounted on glass epoxy PC board with 1.3mm<sup>2</sup> solder pad.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.



# UG2KB05 THRU UG2KB100

Reverse Voltage - 50 to 1000 Volts Forward Current - 2.0 Amperes

## Ratings And Characteristic Curves

Fig. 1 Output Current Derating Curve

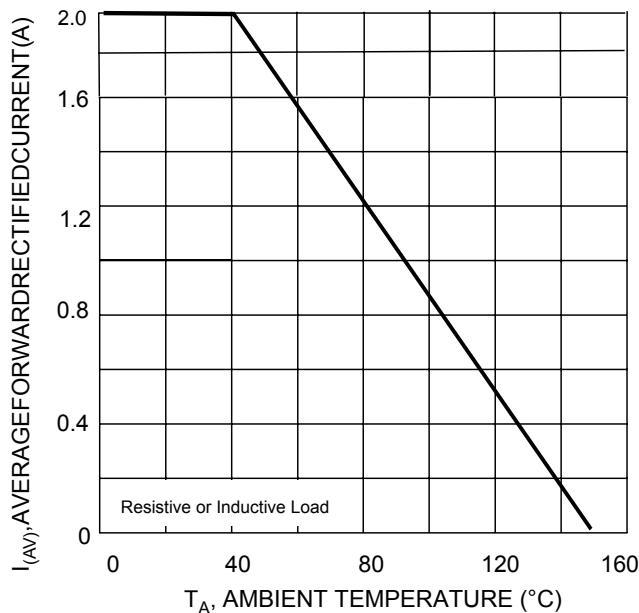


Fig. 2 Typical I Forward Characteristics (per leg)

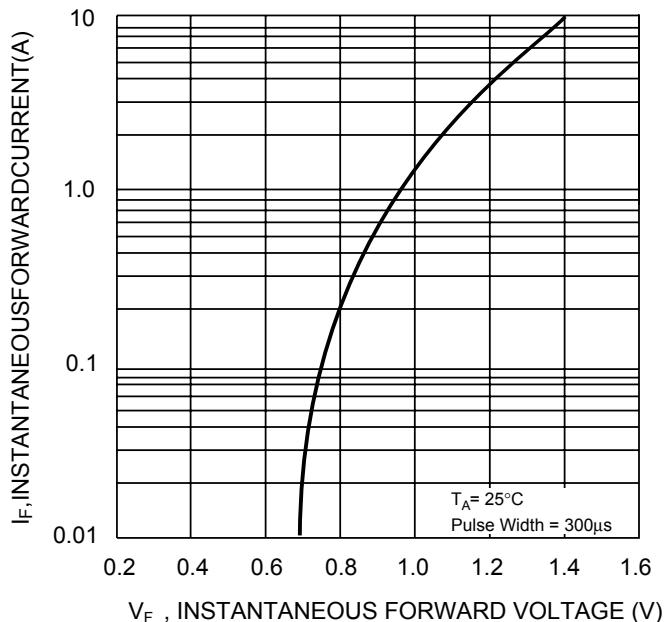


Fig. 3 Maximum Peak Forward Surge Current (per leg)

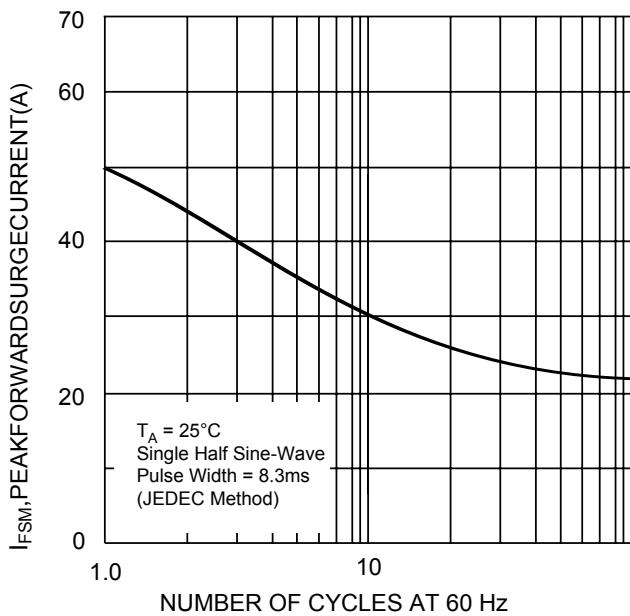
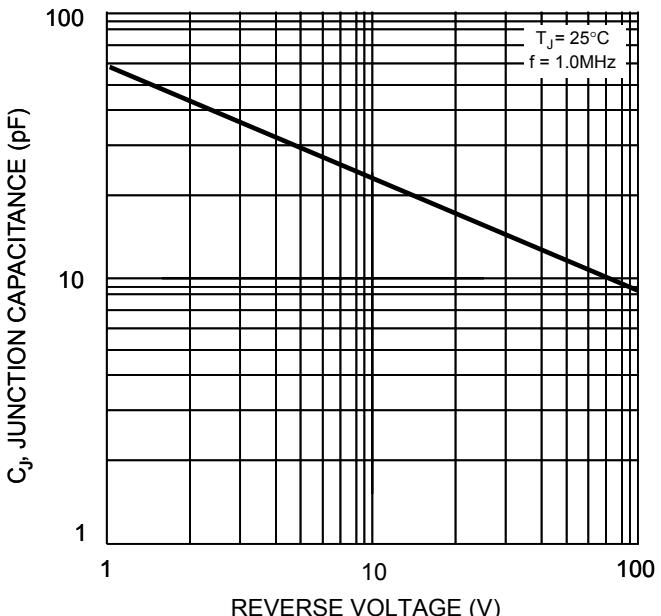


Fig.4 Typical Junction Capacitance Per Diode



The curve above is for reference only.